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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10674606
	Filing Date		2003-09-30
	First Named Inventor	Levine et al.	
	Art Unit	2183	
	Examiner Name	Fiegle, Ryan Paul	
	Attorney Docket Number	AUS920030485US1	

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	4	6961925		2005-11-01	Callahan, II et al.	
	5	5950003		1999-09-30	Kaneshiro et al.	
	6	5212794		1993-05-18	Pettis et al.	
	7	5659679		1997-08-19	Alpert et al.	
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9	6119075		2000-09-12	Dean et al.	
10	6189141	B1	2001-02-13	Benitez et al.	
11	6237141	B1	2001-05-22	Holzle et al.	
12	6351844	B1	2002-02-26	Bala	
13	6505292	B1	2003-01-07	Witt	
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17	5950003		1999-09-07	Kaneshiro et al.	
18	5581778		1996-12-03	Chin et al.	
19	5752062		1998-05-12	Gover et al.	

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	20	6446029	B1	2002-09-03	Davidson et al.	
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	8	AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497.	<input type="checkbox"/>
	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>

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Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2007-03-27
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